



Pentium Microprocessor Trainer

Model CES 586

Model CES 586 Trainer Using 80586 (Pentium) Microprocessor

Model CES 586 Courseware consists of 32 Bit 586 processor based board, Win 9X compatible software, student workbook and Instructor workbook. The software includes Integrated Development system with built in C compiler.

The CES 586 board integrates 586 CPU and a high performance ANSI/IEEE 754 compliant 64-bit hardware floating point unit (FPU).

The FPU provides arithmetic instructions to handle numeric data and transcendental functions for sine, tangent, logarithms, etc, making this controller useful for intensive computational applications. It is estimated to be 10-50 times faster than software-emulate on an 8/16-bit controller without a FPU.

Main Board Specs:

- 80586 CPU
- 512 KB SRAM, 512 KB FLASH, 114 byte internal RAM
- Analog to Digital Converter (TLC2543 from Texas Instrument)
- Digital to Analog Converter (LTC1446 from Linear Technology)
- 2 x 16 LCD Display
- 4x4 Keypad
- 4 Input Sensing Switch
- 4 Digital Inputs
- 4 Digital Outputs
- Serial Port

Trainer Package Includes:

- Trainer Board
- RS232C Cable
- Power Supply
- Win 95/98 Compatible
- Manager Software
- Built-in Editor
- Built-in Assembler
- 'C' Compiler (OPTIONAL)
- 400+ Page Student Workbook
- Instructor Manual includes answers to all exercises
- Expansion Connector for Wire-wrap and other Project Board
- Hundreds of Hardware and Software Exercises

CES 586 Tutor Board supports up to 15 external interrupts. There are a total of seven timers, including one programmable interval timer (PIT) that provides three 16-bit PIT timers and three 16-bit GP timers, plus a software timer. These timers can support timing or counting external events. The software timer provides a very efficient hardware time base with microsecond resolution. A real-time clock (RTC) provides time-of-day, 100-year calendar and 114 bytes of battery backed RAM.

Two industrial-standard 16550-compatible UARTs support baud rates up to 1.152 M baud. One synchronous serial interface (SSI) supports full-duplex bi-directional communication.

CES 586 Tutor Board boots from on-board 256K 16-bit ACTF Flash, and supports up to 256K 16-bit battery-backed SRAM.

There are 32 programmable multifunctional I/O lines (PIO) that can be used as general I/O or other functions. Two supervisor chips monitor 5V and 3.3V and provide power failure detection, watchdog and system reset. The 2.5V is used for the CPU CORE and 3.3V for the I/O operation. Signal lines on headers are 3.3V output, and 5V maximum input.



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